



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Bachelor of Technology (Electronics and Instrumentation)
Choice Based Credit System (CBCS)

SEMESTER - VIII

S. No.	COURSE CODE	COURSE NAME	EXAMINATION SCHEME					TEACHING SCHEME/WEEK			CREDITS	TOTAL MARKS
			THEORY			PRACTICAL		Th	T	P		
			End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)					
1	BTEI801	Project/Industrial Internship	0	0	0	300	200	0	0	32	16	500
			0	0	0	300	200	0	0	32	16	500

*Either Student has to undergo Industrial Internship or Project.

(Signature)

Chairperson
Board of Studies
 Shri Vaishnav Vidyapeeth Vishwavidyalaya

(Signature)

Registrar
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 INDORE (M.P.)